



浙江芯芯电子有限公司  
ZHEJIANG XINXIN ELECTRICAL CO., LTD.

## 产品规格书

### Specification of Products

产品名称：单相可控桥式模块

产品型号：MFQA40A

浙江芯芯电子有限公司

ZHEJIANG XINXIN ELECTRICAL CO., LTD.

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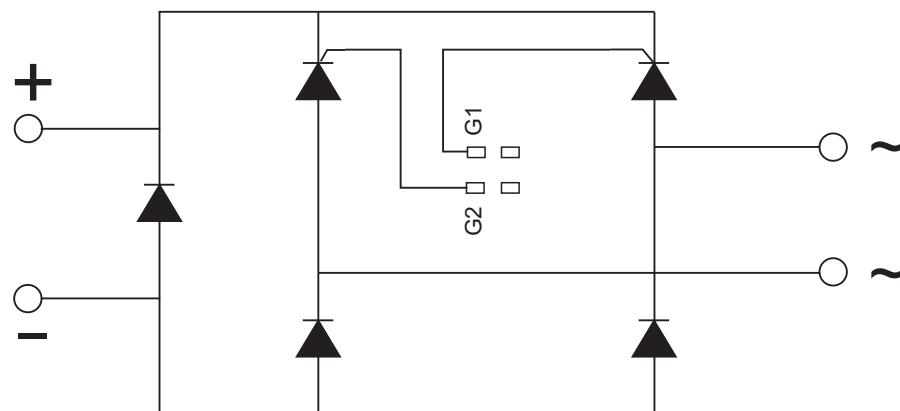
<http://www.zjxxdz1.com>

拟制	审核	核准
丁国盛	李园利	麻伟阳

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SYMBOL	CHARACTERISTIC	TEST CONDITIONS	$T_J$ (°C)	VALUE			UNIT
				Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180° half sinewave 50Hz Single side cooled, $T_c=85^\circ\text{C}$	125			40	A
$I_{T(RMS)}$	RMS on-state current	Single side cooled, $T_c=85^\circ\text{C}$	125			63	A
$V_{DRM}$ $V_{RRM}$	Repetitive peak off-state voltage Repetitive peak reverse voltage	$V_{DRM} \& V_{RRM}$ tp=10ms $V_{DsM} \& V_{RsM} = V_{DRM} \& V_{RRM} + 200\text{V}$ respectively	125	600		1800	V
$I_{DRM}$ $I_{RRM}$	Repetitive peak current	at $V_{DRM}$ at $V_{RRM}$	125			8	mA
$I_{TSM}$	Surge on-state current	10ms half sinewave	125			1.00	KA
$I^2t$	$I^2T$ for fusing coordination	$V_R=60\%V_{RRM}$				5.10 $\text{A}^2\text{s} \times 10^3$	
$V_{TO}$	Threshold voltage		125			0.85	V
$r_T$	On-state slop resistance					5.57	$\text{m}\Omega$
$V_{FM}$	Peak forward voltage	$I_{FM}=40\text{A}$	25			1.1	V
$V_{TM}$	Peak on-state voltage	$I_{TM}=40\text{A}$	25			1.4	V
$dv/dt$	Critical rate of rise of off-state voltage	$V_{DM}=67\%V_{DRM}$	125			500	$\text{V}/\mu\text{s}$
$di/dt$	Critical rate of rise of on-state current	From 67% $V_{DRM}$ to 120A, Gate source 1.5A $t_r \leq 0.5 \mu\text{s}$ Repetitive	125			100	$\text{A}/\mu\text{s}$
$I_{GT}$	Gate trigger current		25	30		80	mA
$V_{GT}$	Gate trigger voltage	$V_A=12\text{V}$ , $I_A=1\text{A}$		0.7		1.5	V
$I_H$	Holding current			20		100	mA
$R_{th(j-c)}$	Junction to heatsink	Single side cooled				0.75	$^\circ\text{C}/\text{W}$
$V_{iso}$	Isolation voltage	50Hz, RM. S, $t=1\text{min}$ $I_{iso}: 1\text{mA (max)}$		2500			V
$F_m$	Terminal connection torque						N.m
	Mounting torque M4)					1.5	N.m
$T_{Stg}$	Stored temperature			-40		125	$^\circ\text{C}$
$W_t$	Weight					160	g
Outline							

## OUTLINE DRAWING &amp; CIRCUIT DIAGRAM



## Rating and Characteristic

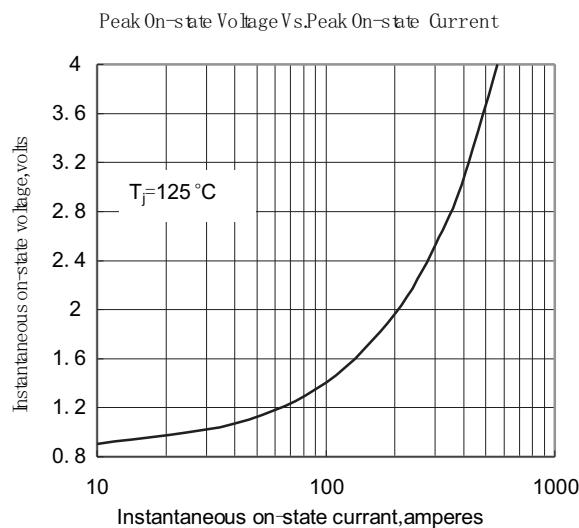


Fig. 1

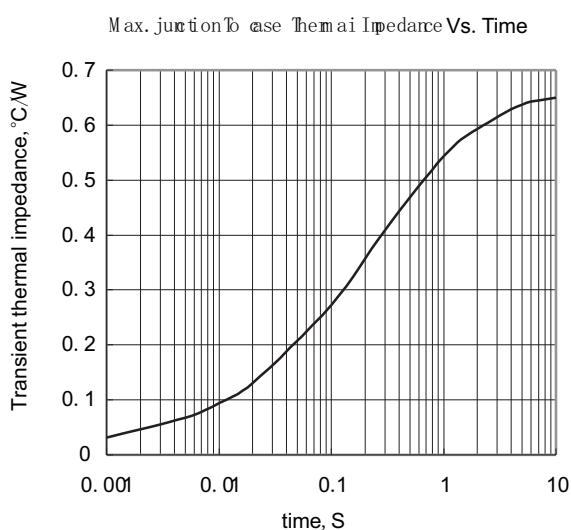


Fig. 2

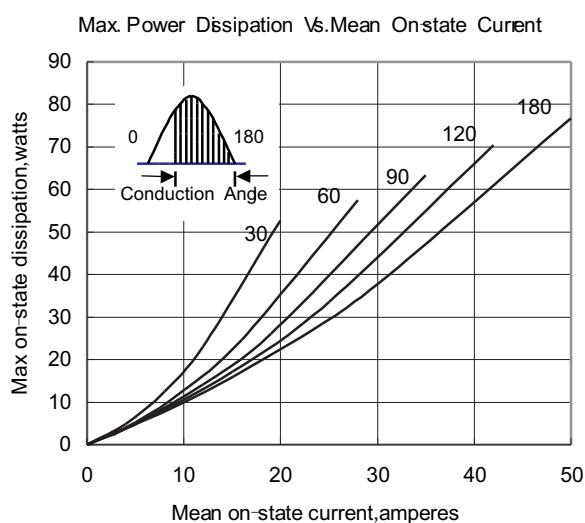


Fig. 3

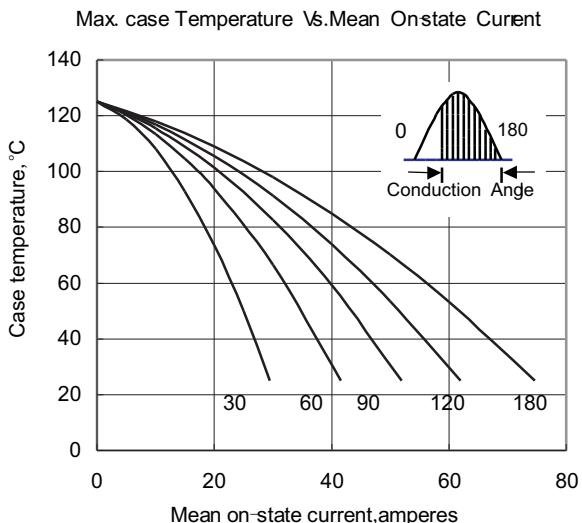


Fig. 4

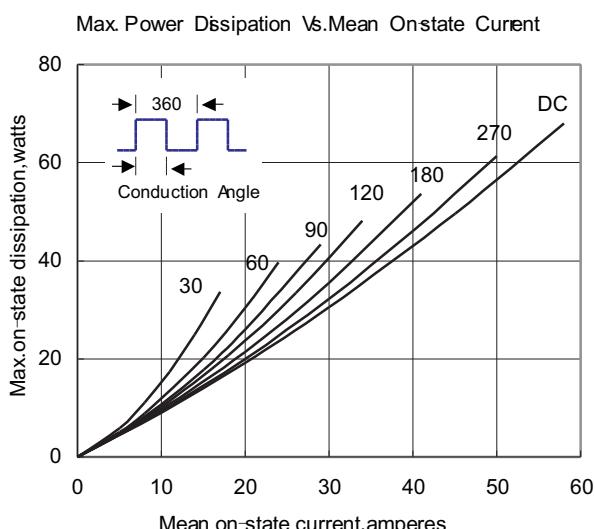


Fig. 5

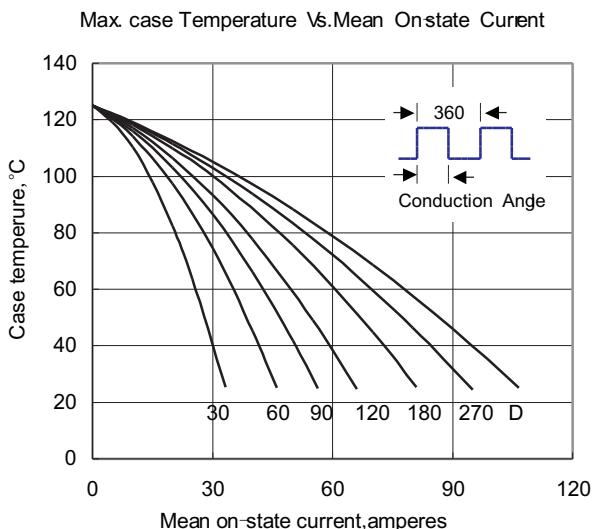


Fig. 6

Rating and Characteristic

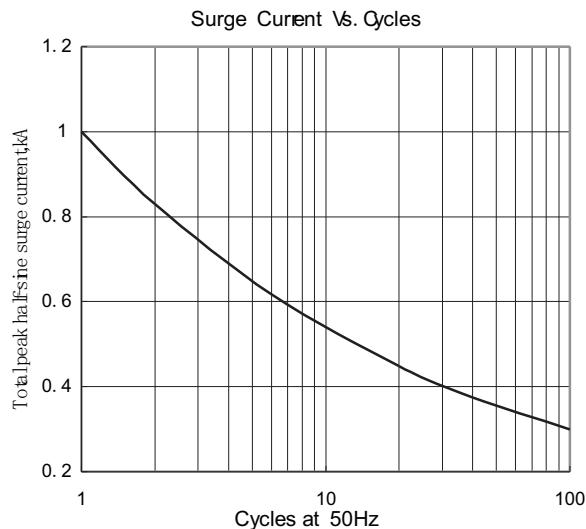


Fig. 7

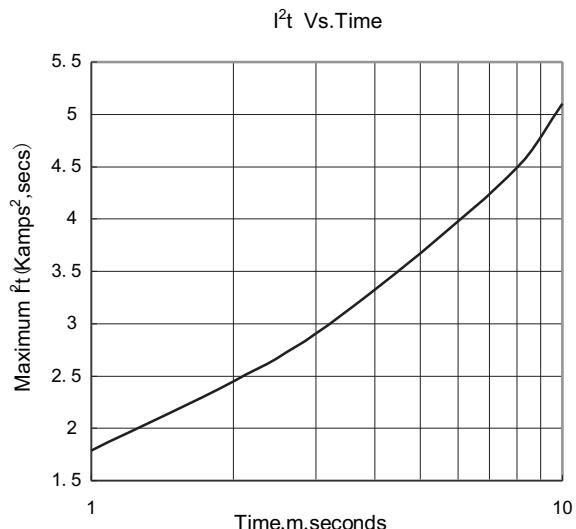


Fig. 8

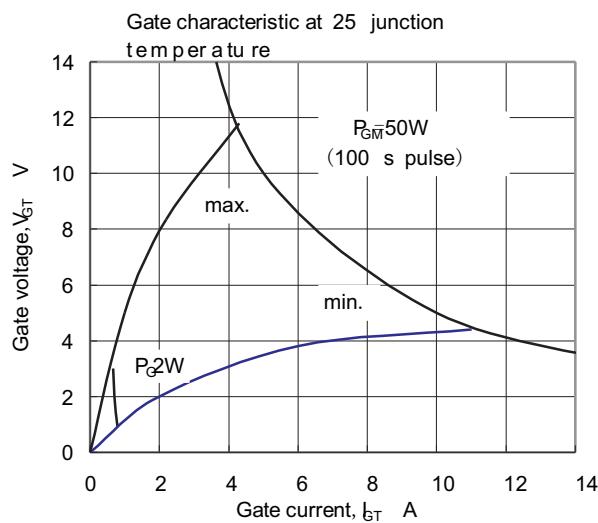


Fig. 9

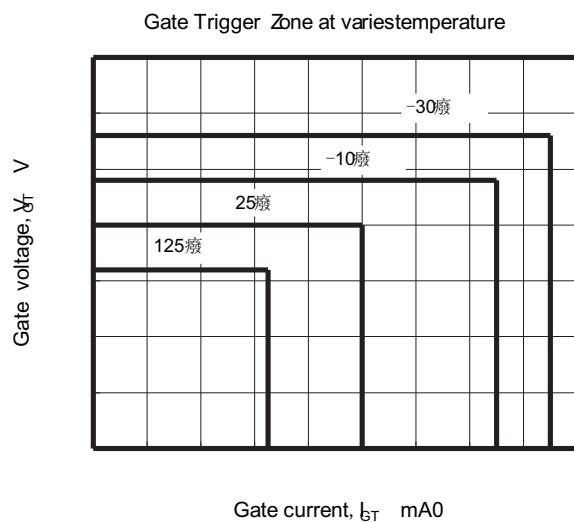


Fig. 10

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# Outside Dimension

