



浙江芯芯电子有限公司  
ZHEJIANG XINXIN ELECTRICAL CO., LTD.

## 产品规格书

### Specification of Products

产品名称：可控硅模块

产品型号：MTC300A

浙江芯芯电子有限公司

ZHEJIANG XINXIN ELECTRICAL CO., LTD.

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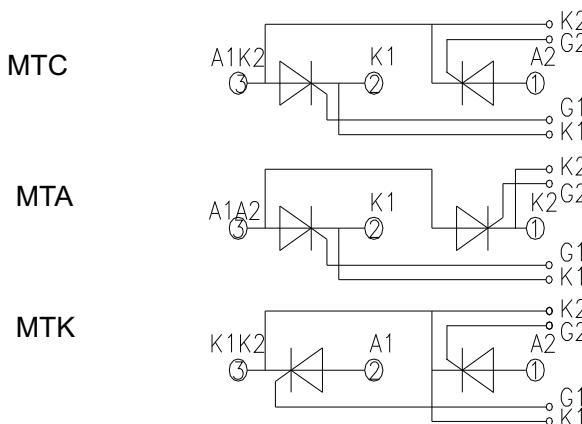
<http://www.zjxxdz1.com>

拟制	审核	核准
丁国盛	李园利	麻伟阳

版本号：04 更新日期：2022.6.28

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	$T_j$ (°C)	VALUE			UNIT
				Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180° half sinewave 50Hz Single side cooled, $T_c=85^\circ\text{C}$	125			300	A
$I_{T(RMS)}$	RMS on-state current	Single side cooled, $T_c=85^\circ\text{C}$	125			471	A
$V_{DRM}$ $V_{RRM}$	Repetitive peak off-state voltage Repetitive peak reverse voltage	$V_{DRM} \& V_{RRM}$ tp=10ms $V_{DsM} \& V_{RsM} = V_{DRM} \& V_{RRM} + 200\text{V}$ respectively	125	600		1800	V
$I_{DRM}$ $I_{RRM}$	Repetitive peak current	at $V_{DRM}$ at $V_{RRM}$	125			30	mA
$I_{TSM}$	Surge on-state current	10ms half sinewave	125			7.20	KA
$I^2t$	$\tilde{I}^2T$ for fusing coordination	$V_R=60\%V_{RRM}$				264 $\tilde{\text{A}}\text{s} \times 10^3$	
$V_{TO}$	Threshold voltage		125			0.80	V
$r_T$	On-state slop resistance					1.27	$\text{m}\Omega$
$V_{TM}$	Peak on-state voltage	$I_{TM}=600\text{A}$	125			1.5	V
$dv/dt$	Critical rate of rise of off-state voltage	$V_{DM}=67\%V_{DRM}$	125			800	V/ $\mu\text{s}$
$di/dt$	Critical rate of rise of on-state current	From 67% $V_{DRM}$ To 600A, Gate source 1.5A $t_r \leq 0.5\text{ }\mu\text{s}$ Repetitive	125			150	A/ $\mu\text{s}$
$I_{GT}$	Gate trigger current		25	30		180	mA
$V_{GT}$	Gate trigger voltage	$V_A=12\text{V}, I_A=1\text{A}$		1.0		2.5	V
$I_H$	Holding current			20		100	mA
$V_{GD}$	Non-trigger gate voltage	At 67% $V_{DRM}$	125			0.2	V
$R_{th(j-c)}$	Thermal resistance Junction to heatsink	At 180° sine Single side cooled				0.140	$^\circ\text{C}/\text{W}$
$V_{iso}$	Isolation voltage	50Hz, RM. S, t=1min, $i_{lo}: 1\text{mA MAX}$ )		2500			V
$F_m$	Thermal connection torque(M8)				4.5		N.m
	Mounting torque(M6)				3.0		N.m
$T_{stg}$	Stored temperature			-40		140	$^\circ\text{C}$
$W_t$	Weight				1100		g
Outline							

## OUTLINE DRAWING &amp; CIRCUIT DIAGRAM



### Rating and Characteristic

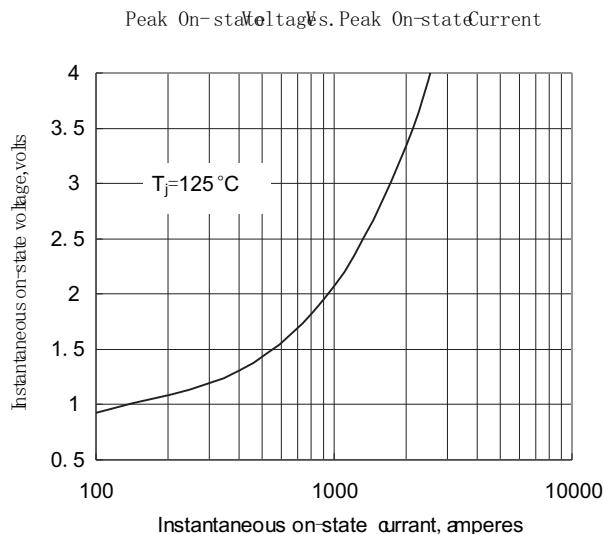


Fig. 1

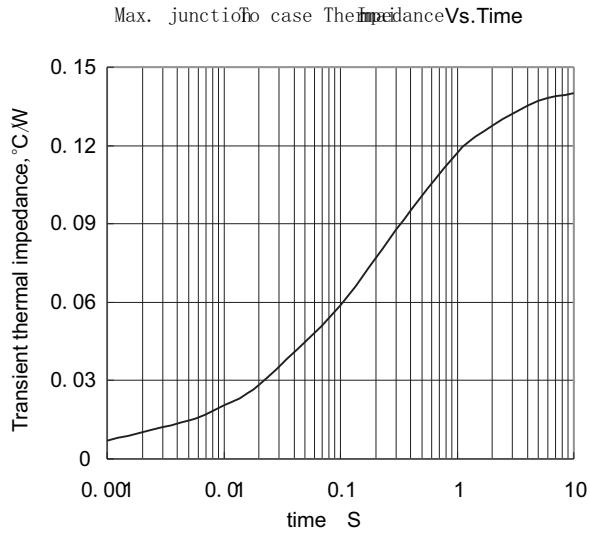


Fig. 2

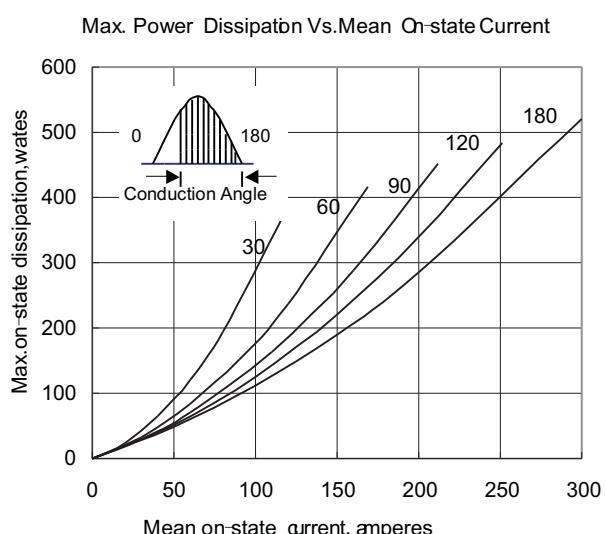


Fig. 3

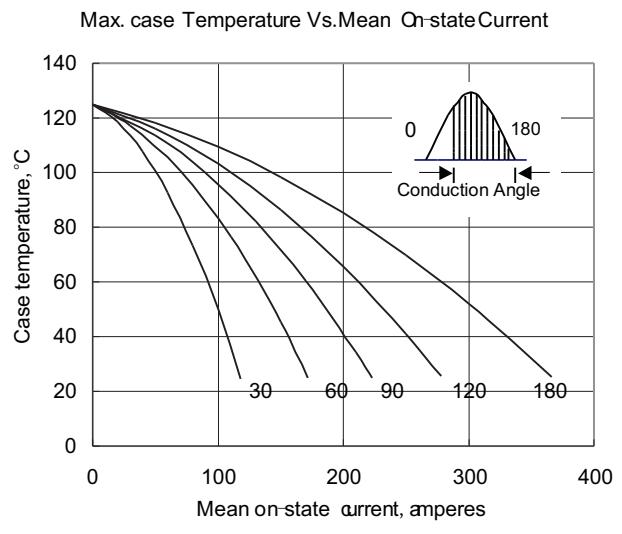


Fig. 4

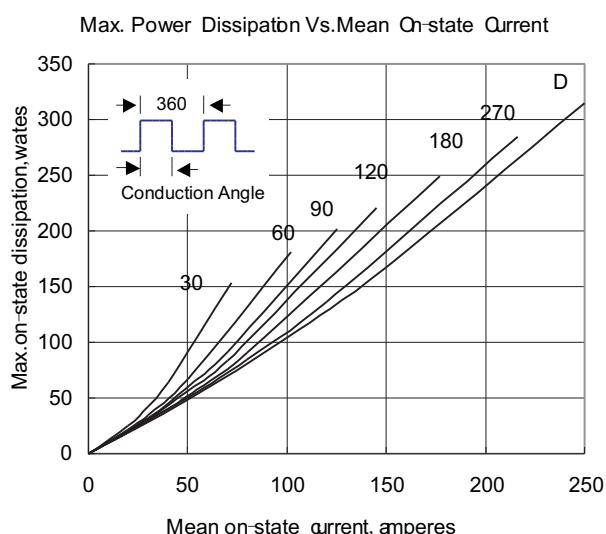


Fig. 5

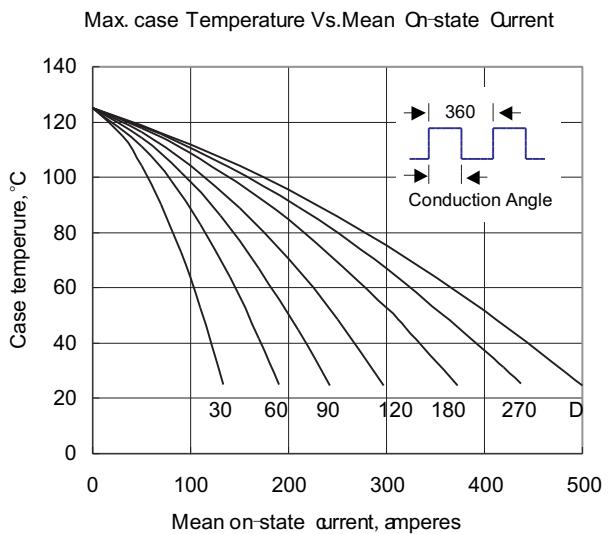


Fig. 6

Rating and Characteristic

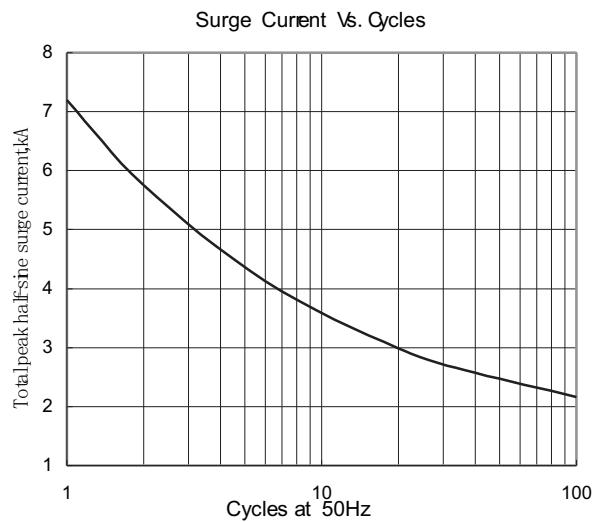


Fig. 7

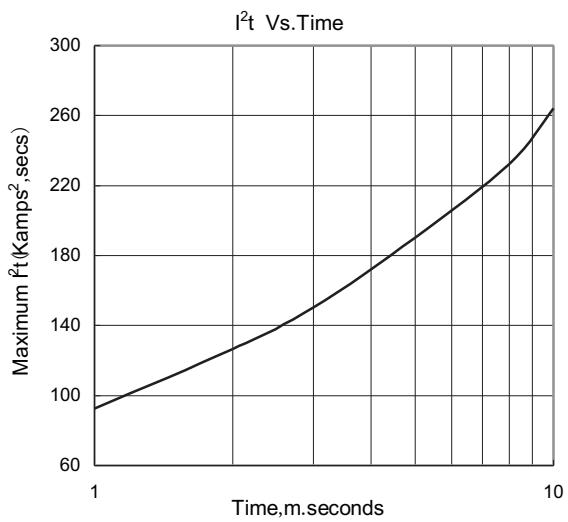


Fig. 8

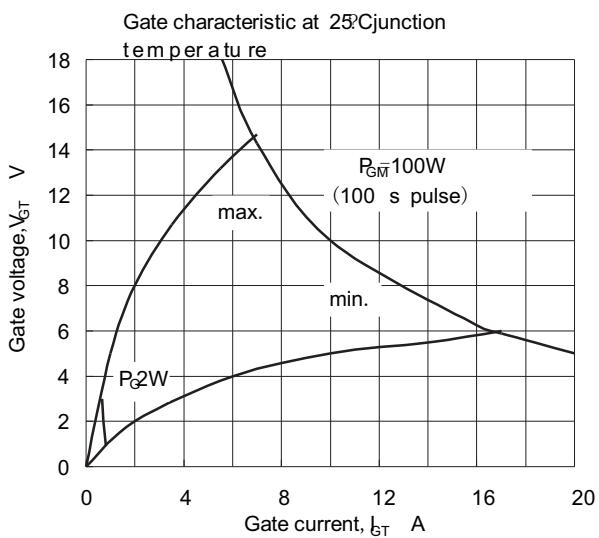


Fig. 9

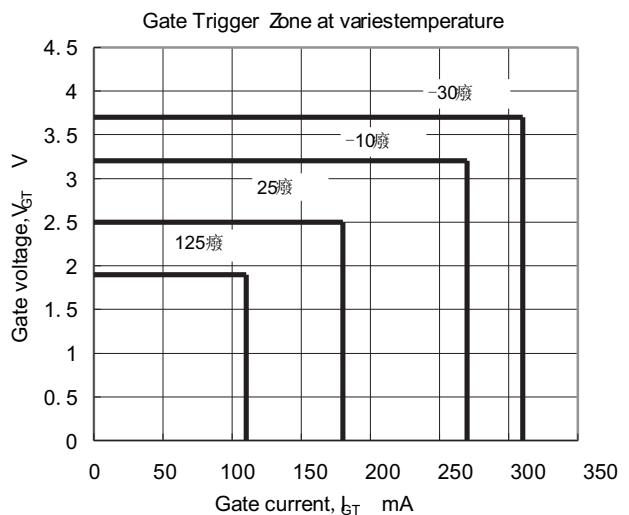


Fig. 10

## Outside Dimension

