



浙江芯芯电子有限公司  
ZHEJIANG XINXIN ELECTRICAL CO., LTD.

## 产品规格书

### Specification of Products

产品名称：可控硅模块

产品型号：MTG250A

浙江芯芯电子有限公司

ZHEJIANG XINXIN ELECTRICAL CO., LTD.

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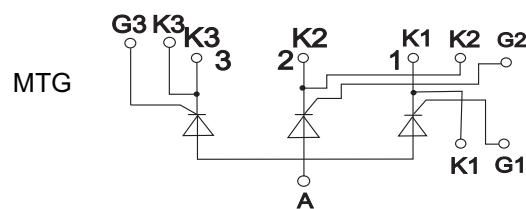
<http://www.zjxxdz1.com>

拟制	审核	核准
丁国盛	李园利	麻伟阳

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SYMBOL	CHARACTERISTIC	TEST CONDITIONS	$T_j$ (°C)	VALUE			UNIT
				Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180° half sinewave 50Hz Single side cooled, $T=90^\circ\text{C}$	125			250	A
$I_{T(RMS)}$	RMS on-state current	Single side cooled, $T=90^\circ\text{C}$	125			393	A
$V_{DRM}$ $V_{RRM}$	Repetitive peak offstate voltage Repetitive peak reverse voltage	$V_{DRM} \& V_{RRM}$ tp=10ms $V_{DSM} \& V_{RSM} = V_{DRM} \& V_{RRM} + 200\text{V}$ respectively	125	800		1800	V
$I_{DRM}$ $I_{RRM}$	Repetitive peak current	at $V_{DRM}$ at $V_{RRM}$	125			20	mA
$I_{TSM}$	Surge on-state current	10ms half sine wave	125			8.50	KA
$I^2t$	$I^2t$ for fusing coordination	$V_R=60\%$ $V_{RRM}$				368	$\text{As} \times 10^3$
$V_{TO}$	Threshold voltage		125			0.80	V
$r_T$	On-state slope resistance					1.02	$\text{m}\Omega$
$V_{TM}$	Peak on-state voltage	$I_M=750\text{A}$	125			1.4	V
$dv/dt$	Critical rate of rise of off-state voltage	$V_{DM}=67\%V_{DRM}$	125			800	V/ $\mu\text{s}$
$di/dt$	Critical rate of rise of on-state current	From 67% $V_{DRM}$ to 750A, Gate source 1.5A $t \leq 0.5 \mu\text{s}$ Repetitive	125			100	A/ $\mu\text{s}$
$I_{GT}$	Gate trigger current			30		150	mA
$V_{GT}$	Gate trigger voltage	$V_A=12\text{V}$ , $I_A=1\text{A}$	25	1.0		2.5	V
$I_H$	Holding current			20		100	mA
$V_{GD}$	Non-trigger gate voltage	At 67% $V_{DRM}$	125			0.2	V
$R_{th(j-c)}$	Thermal resistance Junction to heatsink	Single side cooled				0.100	$^\circ\text{C}/\text{W}$
$F_m$	Thermal connection torque (M6)					3.0	N.m
	Mounting torque (M6)					3.0	N.m
$T_{stg}$	Stored temperature			-40		140	$^\circ\text{C}$
$W_t$	Weight					340	g
Outline							

## OUTLINE DRAWING &amp; CIRCUIT DIAGRAM



## Rating and Characteristic

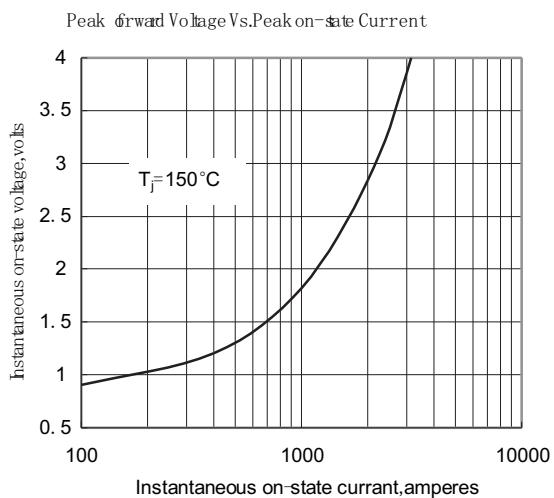


Fig. 1

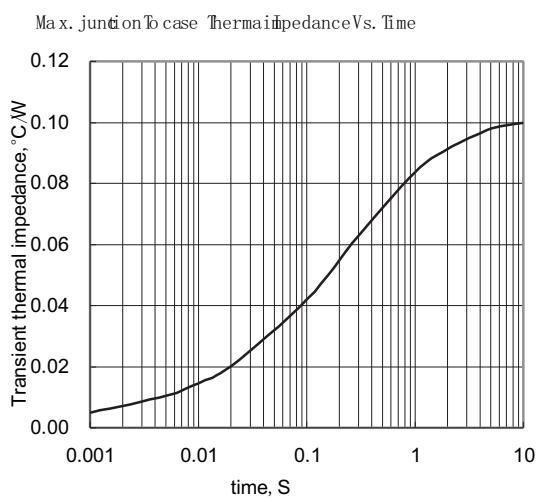


Fig. 2

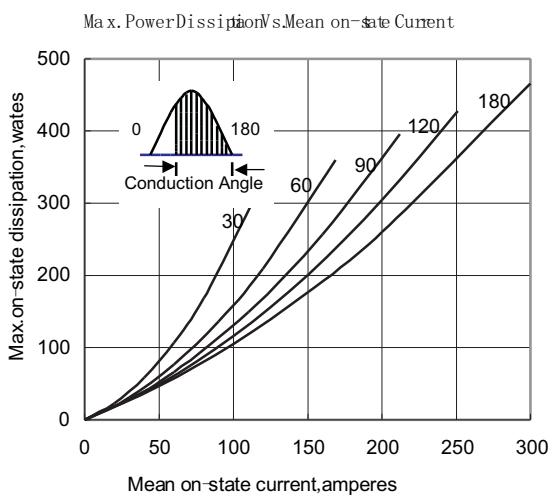


Fig. 3

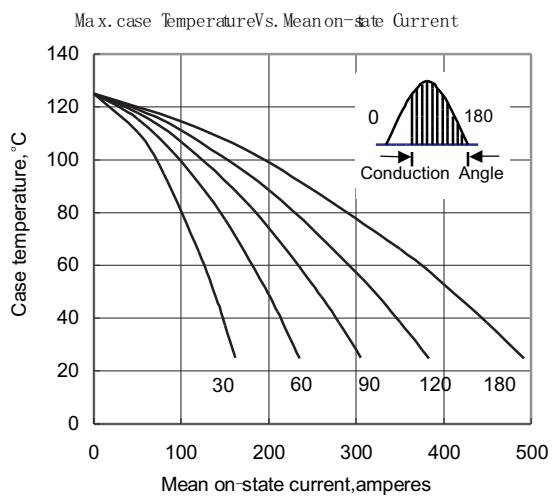


Fig. 4

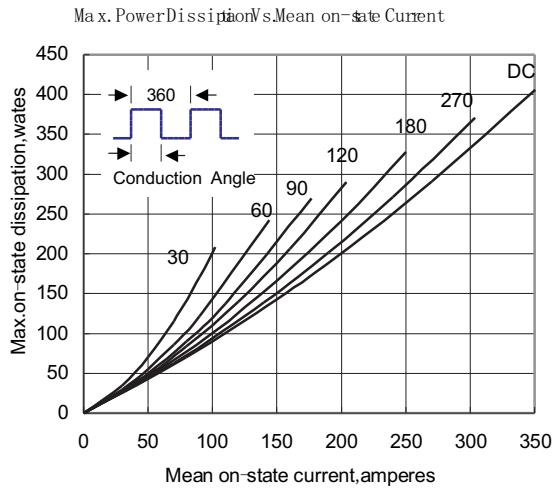


Fig. 5

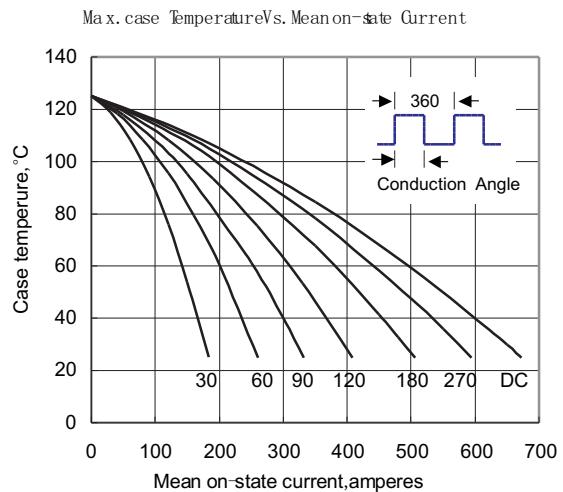


Fig. 6

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## Outside Dimension

